

Call for Papers

SPECIAL ISSUE ON GIGABIT LOGIC FOR MICROWAVE SYSTEMS

The IEEE TRANSACTIONS ON MICROWAVE THEORY AND TECHNIQUES is planning to publish a special issue on gigabit logic for microwave systems in May 1980. Papers are solicited that describe new and significant aspects of research, development, design, or application in the following (but not limited to) areas:

- Depletion and Enhancement Mode Schottky-Barrier GaAs FET Integrated Circuits
- JFET Integrated Circuits
- TELD Circuits
- Si Bipolar and MOSFET High-Speed Integrated Circuits
- Device and Circuit Modeling Techniques for Design and Optimization
- RF Testing Techniques for Design and Optimization
- Radiation Effects on High-Speed Performance
- Microwave Applications to Real-Time Signal Processing
- Matching Considerations Between High-Impedance, Lumped-Element Circuits and Distributed, Low Impedance Circuits.

The deadline for submission of manuscripts is July 15, 1979. Authors are requested to submit five (5) copies of each manuscript to:

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